AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0016] with the following amended paragraph:

[0016] As noted above, the standard packet detector 22 is designed to detect a spread spectrum barker code. The typical barker code is eleven bits long. Detection is performed by cross correlating the known barker code with the incoming signals. When the proper code is received, the correlator produces an easily recognized spike. ~~The spread spectrum barker code does not require that the received RF signals be processed by a high resolution ADC. A low resolution ADC having only two or three bits of resolution has been found to provide a signal which allows a packet detector, e.g. 22, to detect the barker code with essentially no reduction in detection accuracy. However, a low resolution ADC uses considerably less power than a high resolution ADC. Typical stations actually receive incoming data packets for only a small percentage of the time. The stations spend the majority of the time listening for incoming data packets.~~

Please replace paragraph [0017] with the following amended paragraph:

[0017] In Fig. 2, an embodiment of a WLAN receiver system according to the present invention is illustrated. The spread spectrum barker code does not require that the received RF signals be processed by a high resolution ADC. A low resolution ADC having only two or three bits of resolution has been found to provide a signal which allows a packet detector, e.g. 40, to detect the barker code with essentially no reduction in detection accuracy. A low resolution ADC uses considerably less power than a high

resolution ADC. Typical stations actually receive incoming data packets for only a small percentage of the time. The stations spend the majority of the time listening for incoming data packets. This embodiment includes an antenna 30 and RF section 32 ~~which may be the same as elements 10 and 14 of Fig. 1~~. The RF section 32 output is coupled to both an eight-bit ADC 34 and a two-bit ADC 36. Outputs of both ADC 34 and ADC 36 are coupled to a switch 38. The output of the two-bit ADC 36 is also coupled to a packet detector 40~~, which may be the same as packet detector 22 of Fig. 1~~. The output of switch 38 is coupled to a receiver 42 and to an AGC 44~~, which may be the same as corresponding elements 24 and 26 of Fig. 1~~. The output of receiver 42 is coupled to a station 46, e.g. a computer. The packet detector 40 has an output coupled to the eight-bit ADC 34, the switch 38 and the receiver 42.